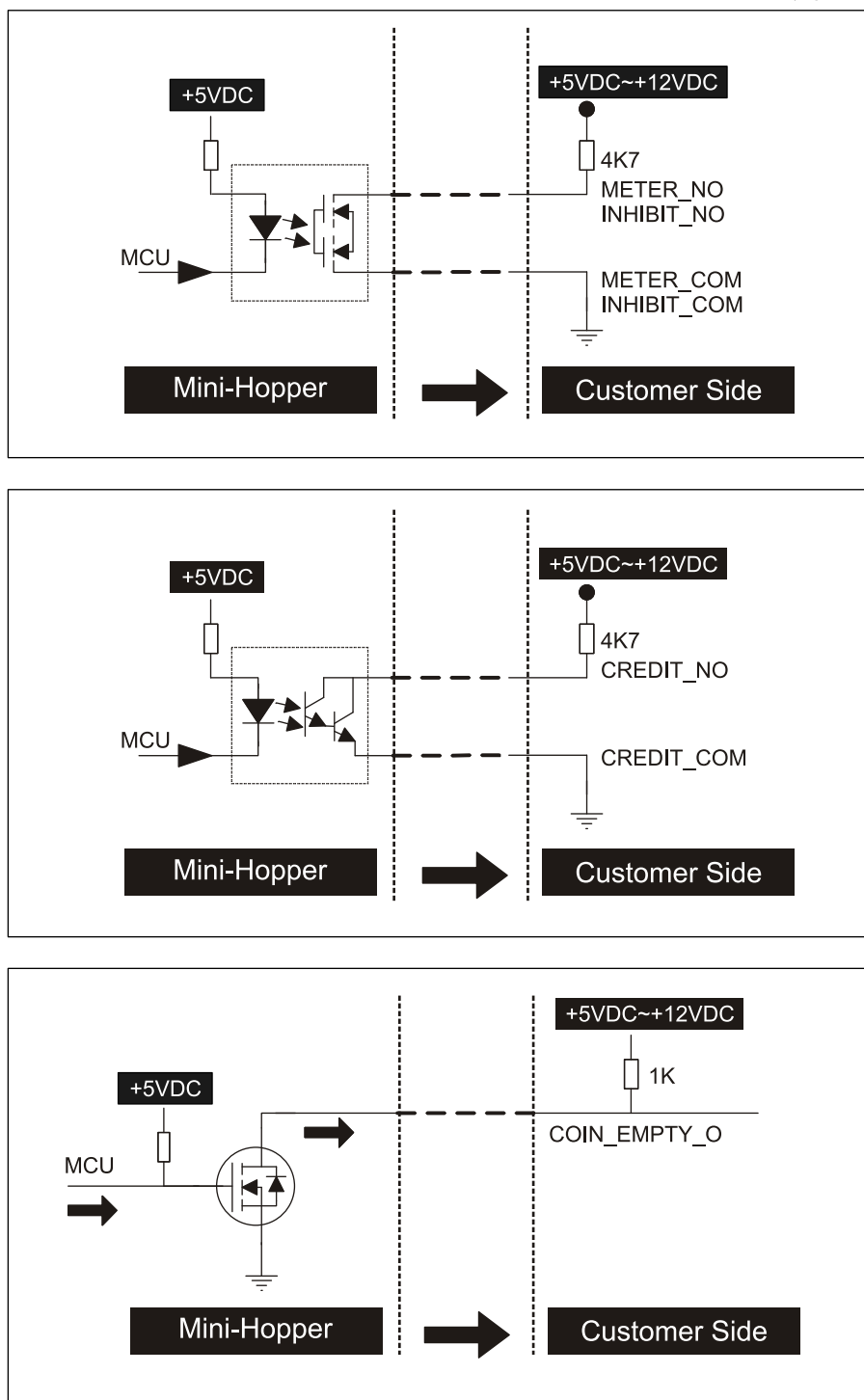


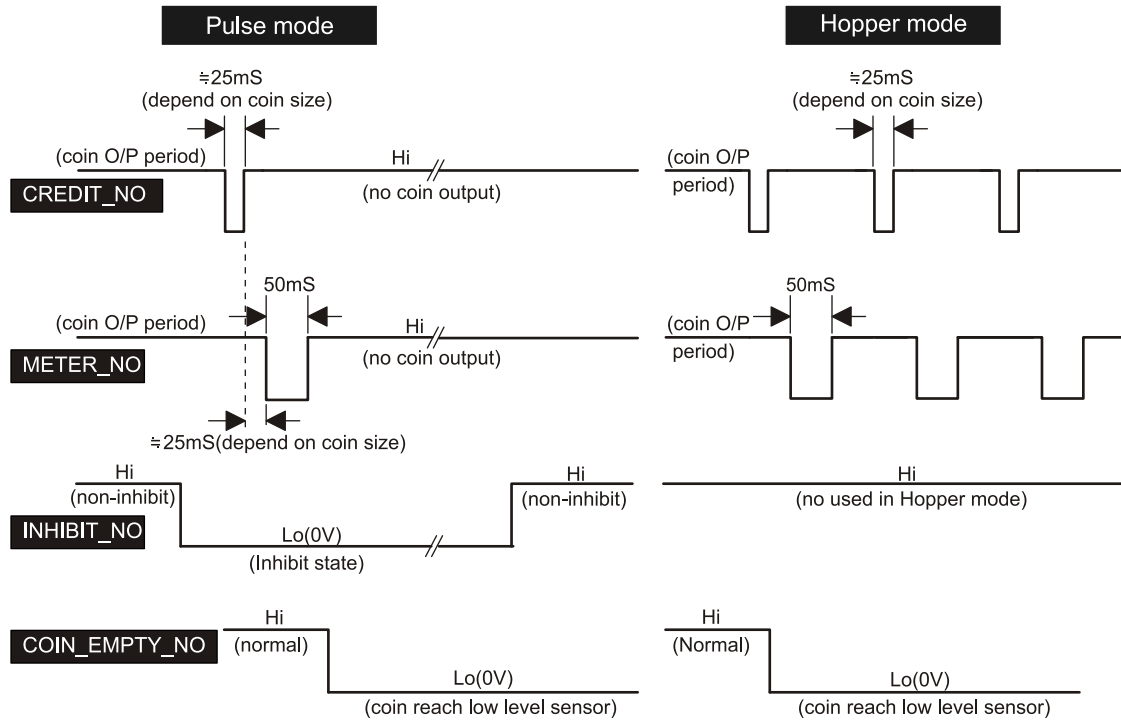
3-2. I/O Circuits

Hopper & Pulse Interface.

(Figure 39)



(Figure 40)



(Figure 41)

