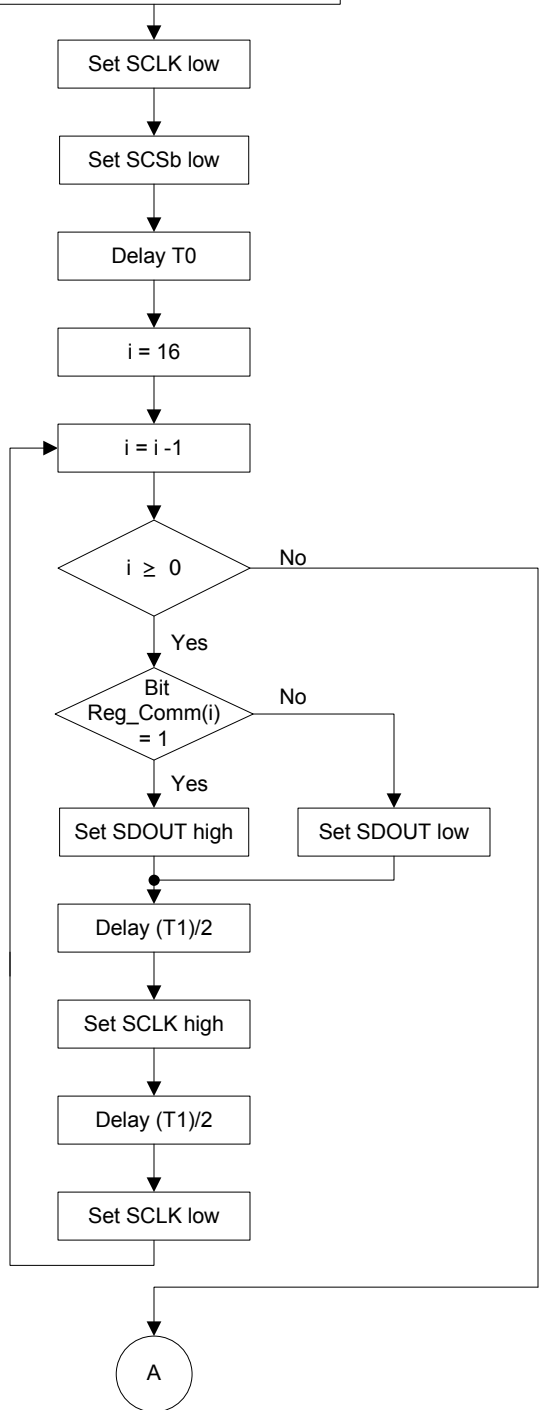


GSPI Read Operation

GSPI_Read(Reg_Comm, Reg_Value)



// Reg_Comm: 16-bit binary, output
Reg_Value: 16-bit binary, input

// SCLK must be low when SCSb changes from high to low

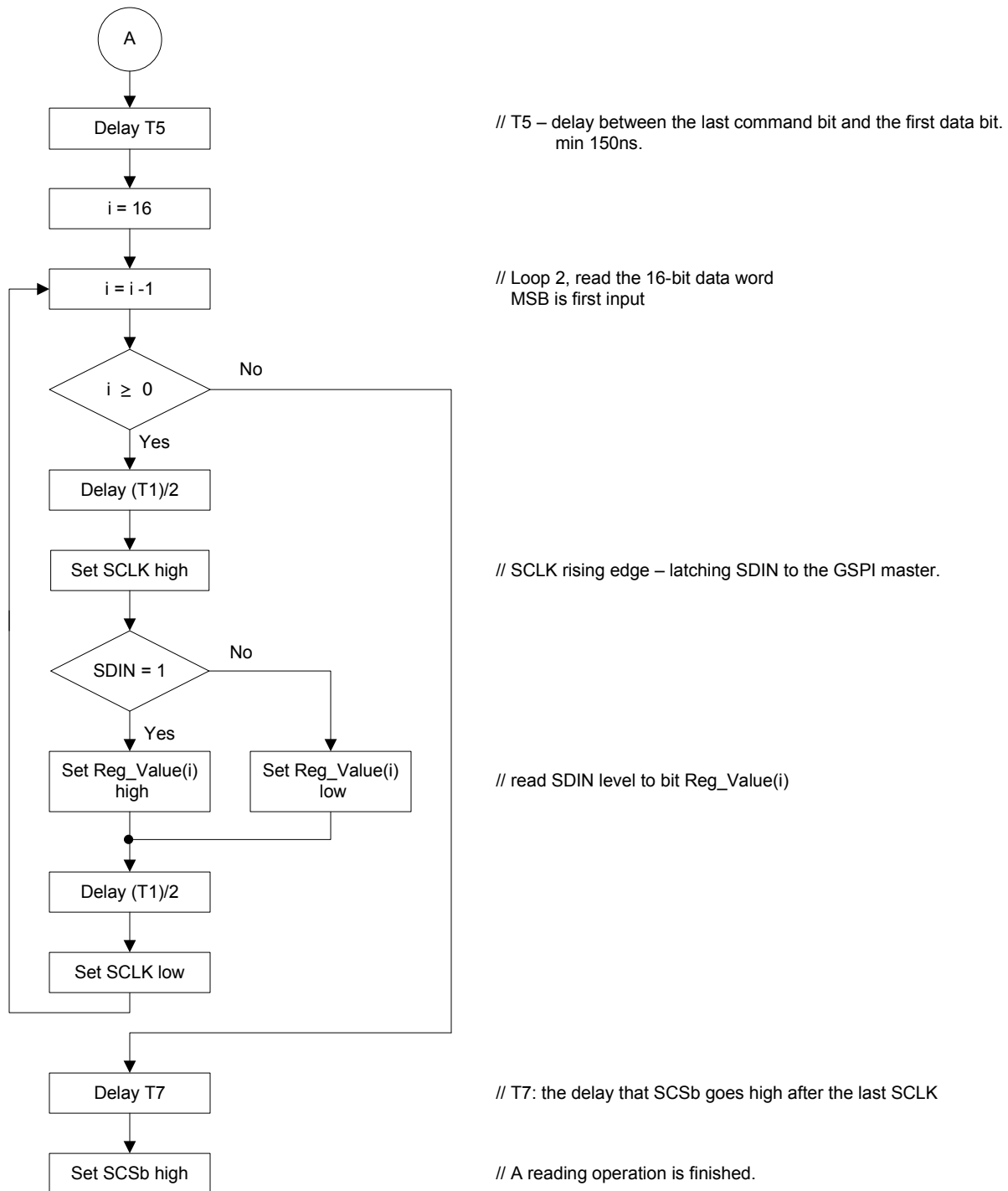
// T0: min 1.5ns since SCSb low to the first SCLK rising edge

// Loop 1, send out the 16-bit command word
Start from MSB

// Set SDOOUT level according to bit Reg_Comm(i)

// T1: SCLK period.

// SCLK rising edge – latching SDOOUT to the GSPI device.

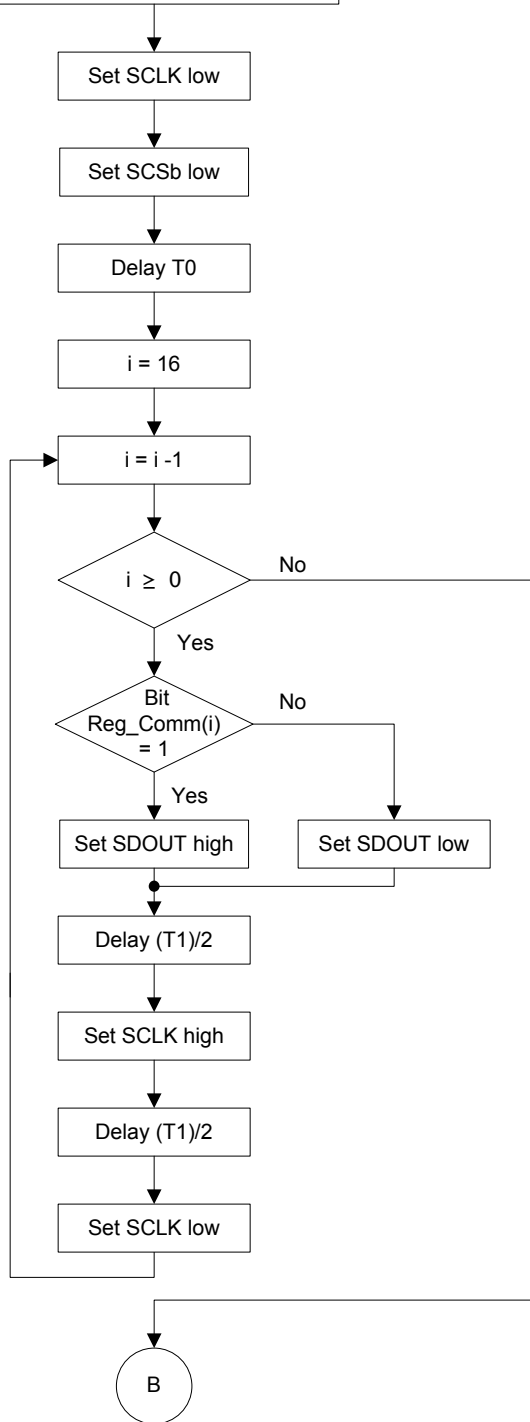


Note:

1. Here, SDIN, SDOUT are the signals from the GSPI master.
2. Please see the device datasheets for the timing parameters T0, T1, T5, T7.

GSPI Write Operation

GSPI_Write(Reg_Comm, Reg_Value)



// Reg_Comm: 16-bit binary, output
Reg_Value: 16-bit binary, output

// SCLK must be low when SCSb changes from high to low

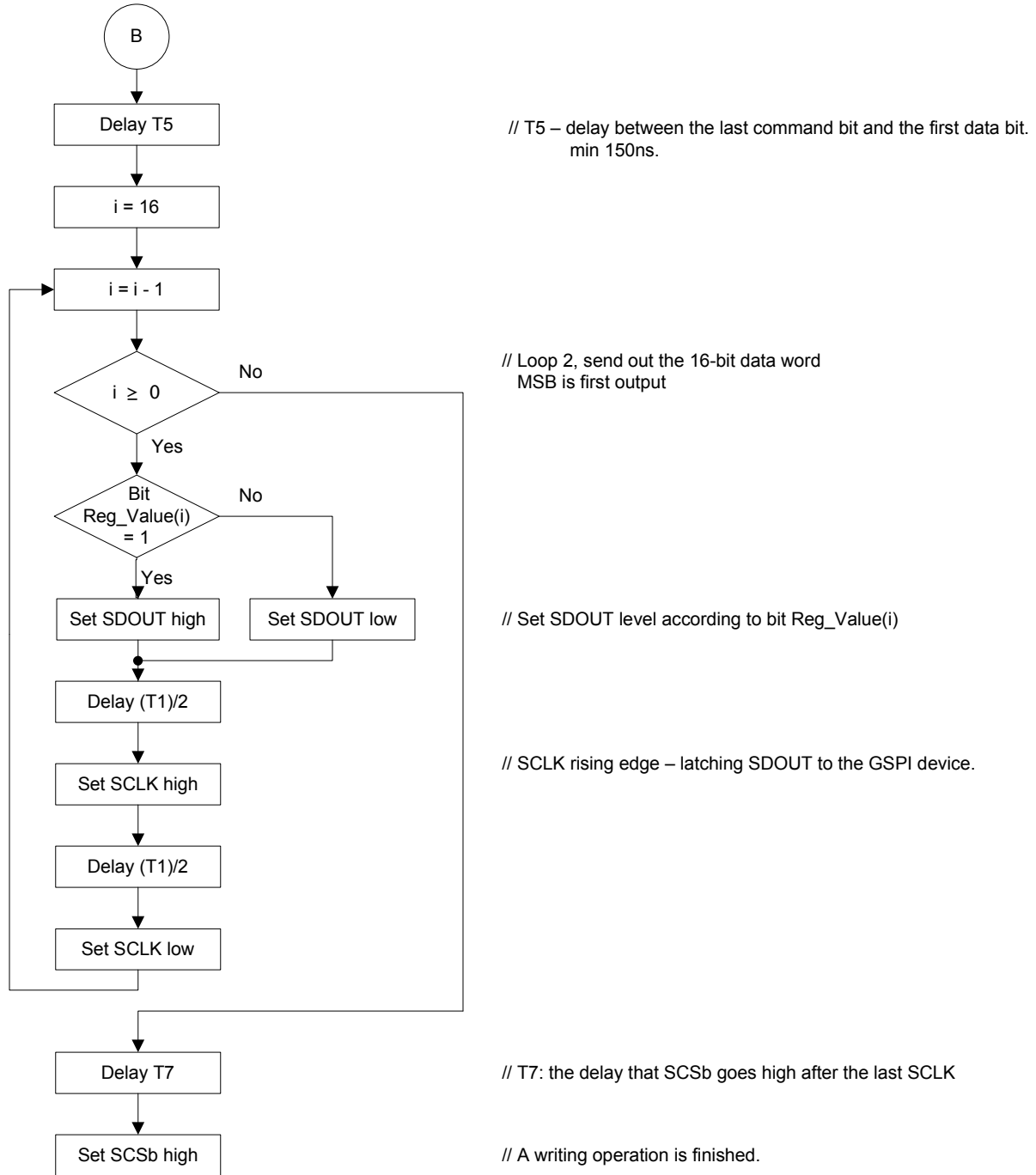
// T0: min 1.5ns since SCSb low to the first SCLK rising edge

// Loop 1, send out the 16-bit command word
Start from MSB

// Set SDOUT level according to bit Reg_Comm(i)

// T1: SCLK period.

// SCLK rising edge – latching SDOUT to the GSPI device.



Note:

1. Here, SDIN, SDOUT are the signals from the GSPI master.
2. Please see the device datasheets for the timing parameters T0, T1, T5, T7.